



TITLE: A METHOD OF FORMING AN INTEGRATED CIRCUIT SUBSTRATE
INVENTORS NAME: Charan K. Gurumurthy et al.
SERIAL NO.: 10/612282

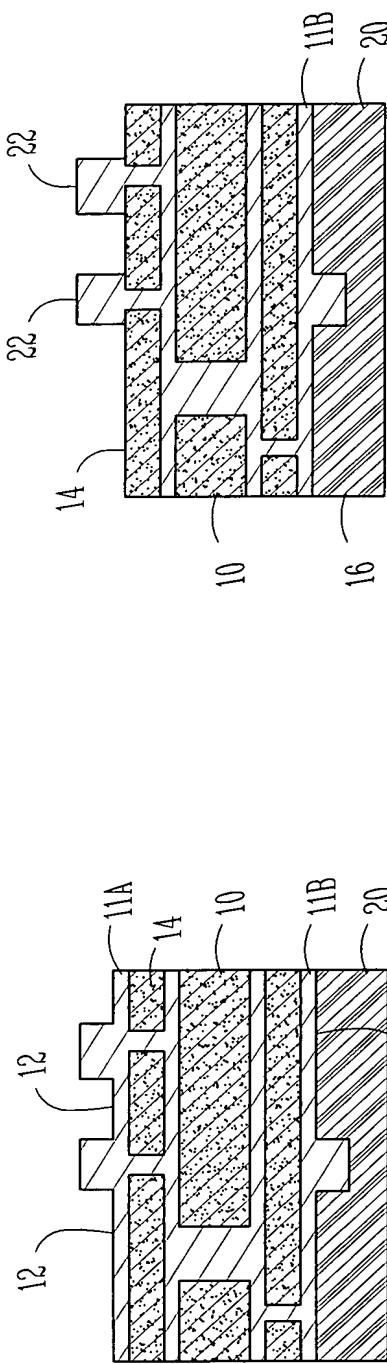


Fig. 1

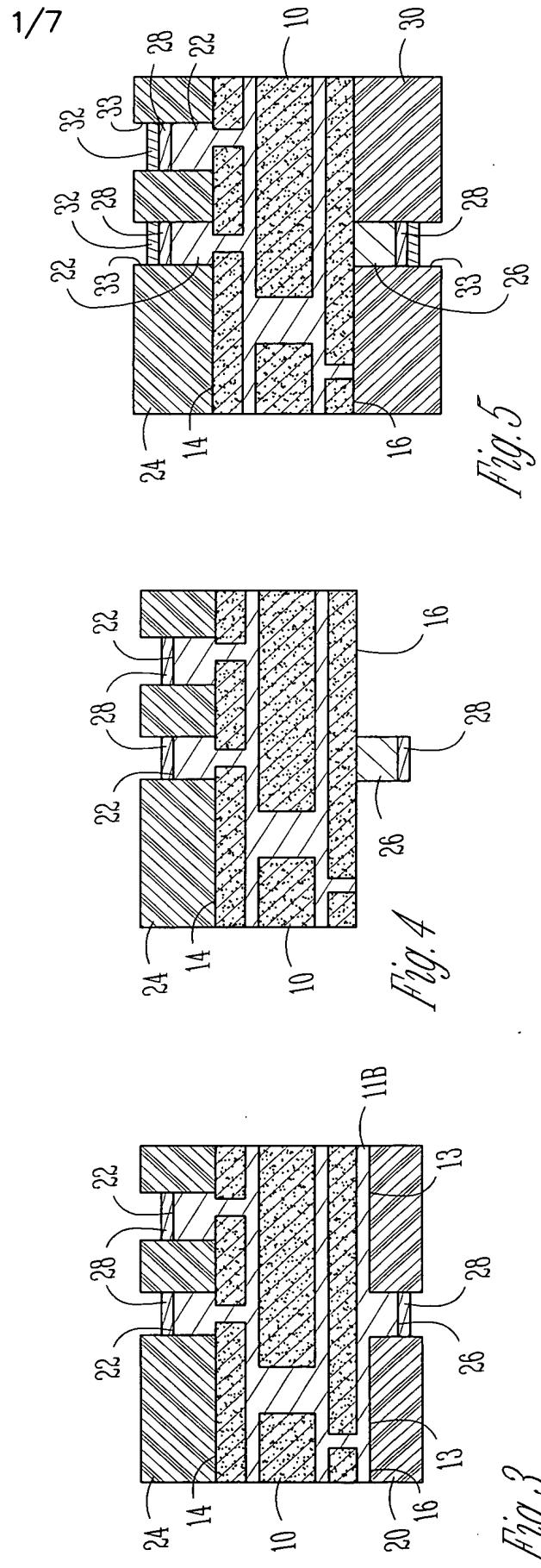


Fig. 2

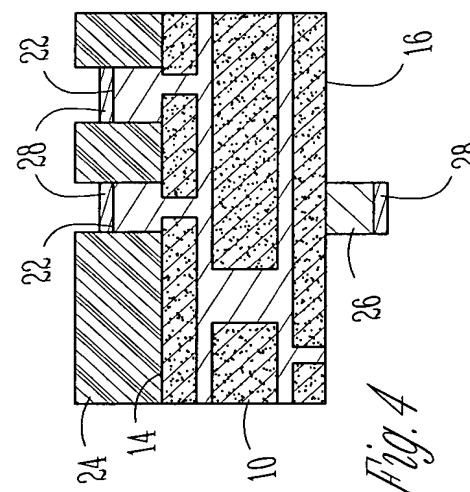


Fig. 3

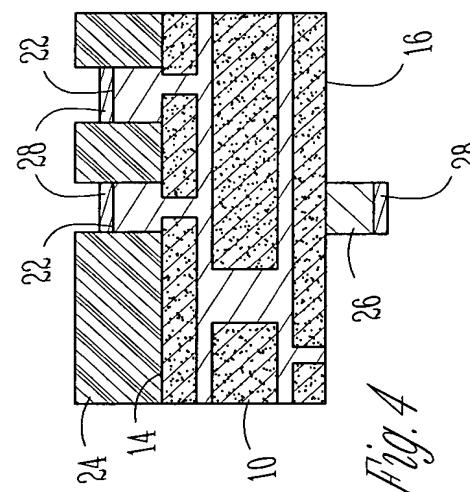


Fig. 4

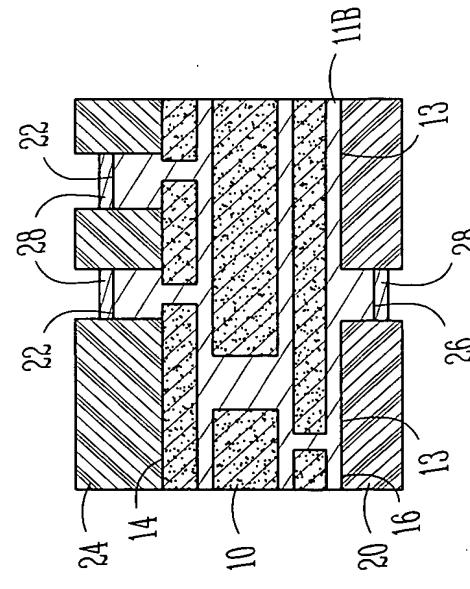


Fig. 5

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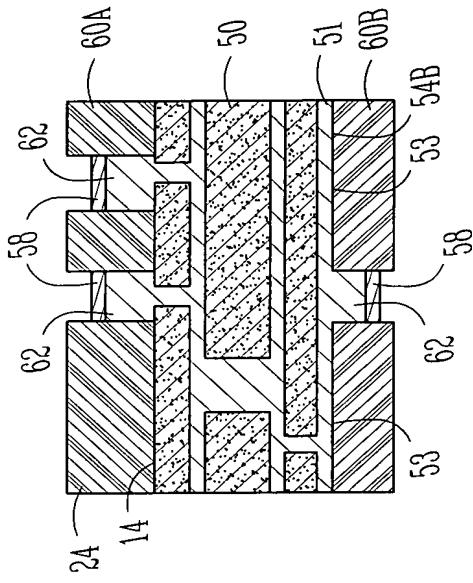


Fig. 7

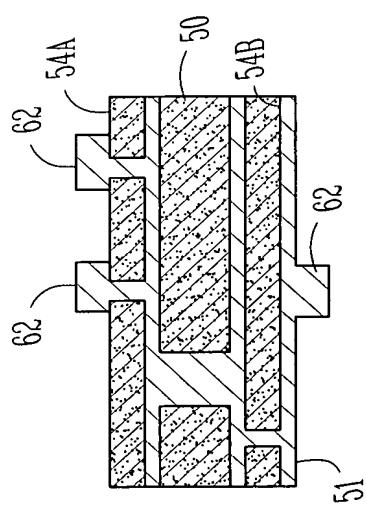


Fig. 6

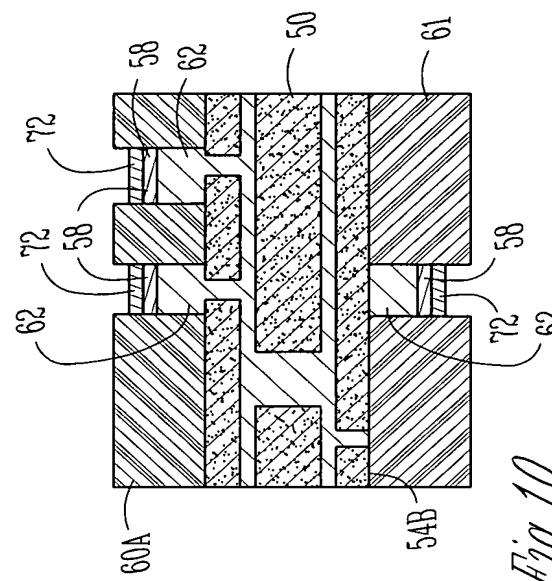


Fig. 10

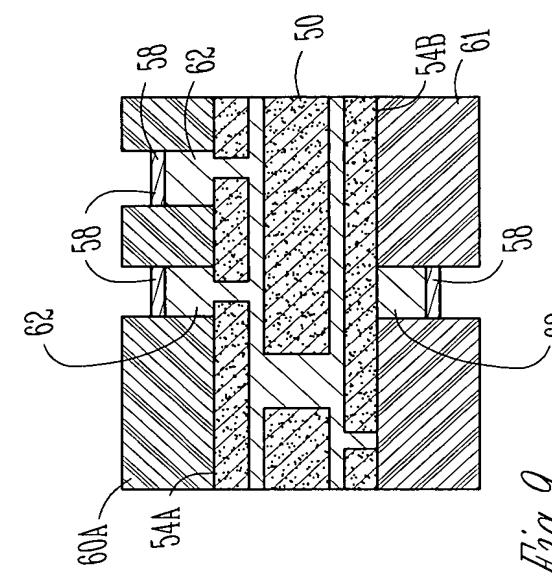


Fig. 9

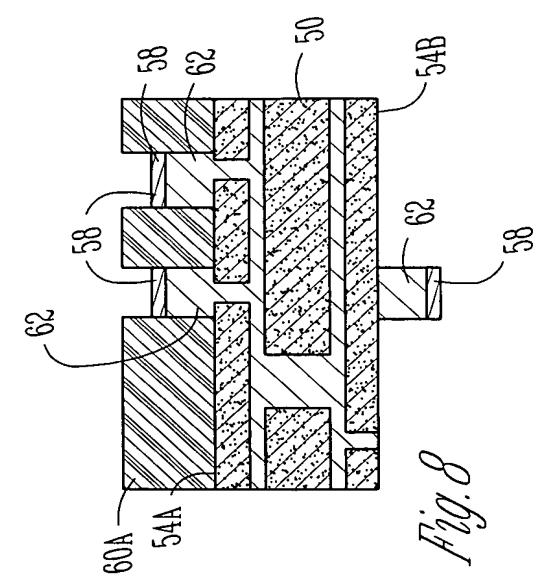


Fig. 8

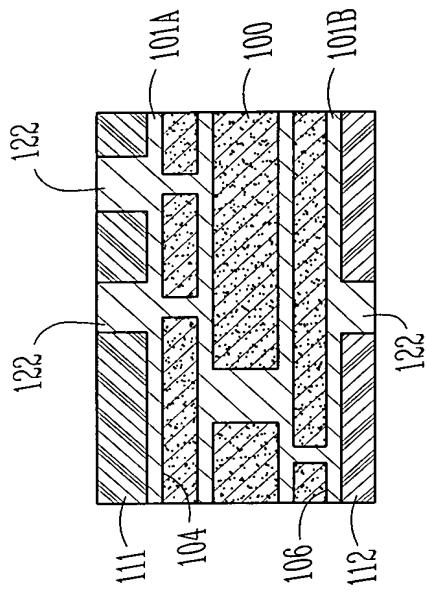


Fig. 11

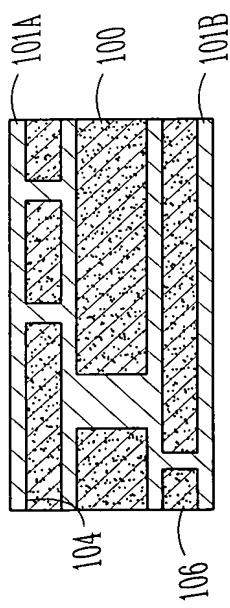


Fig. 12

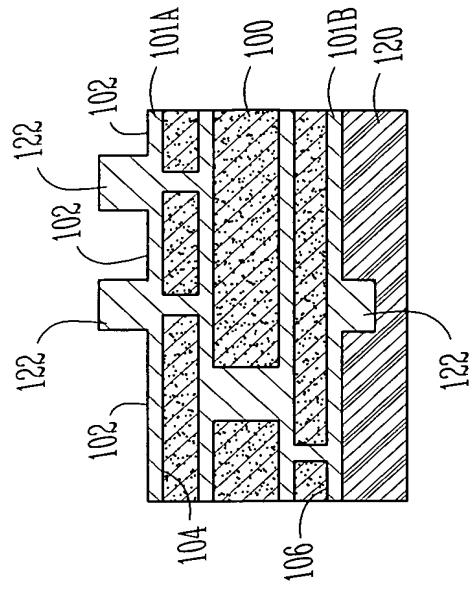


Fig. 13

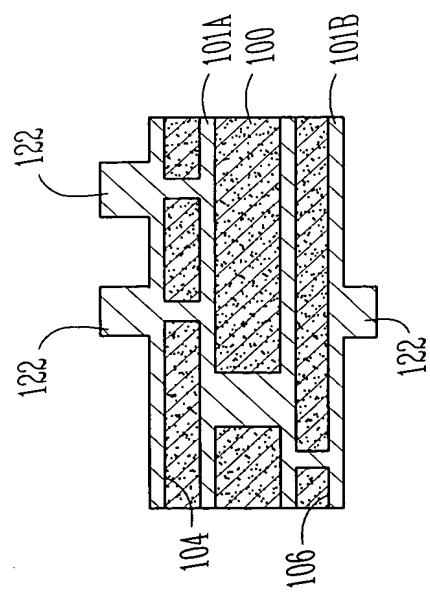
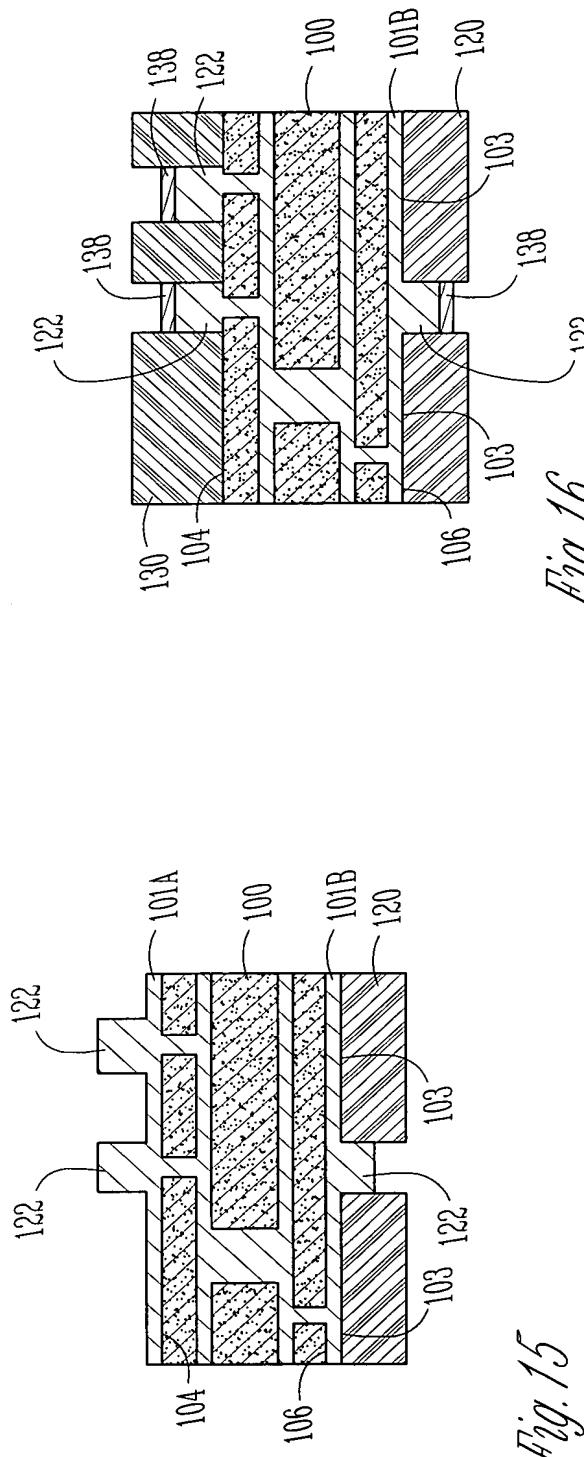


Fig. 14

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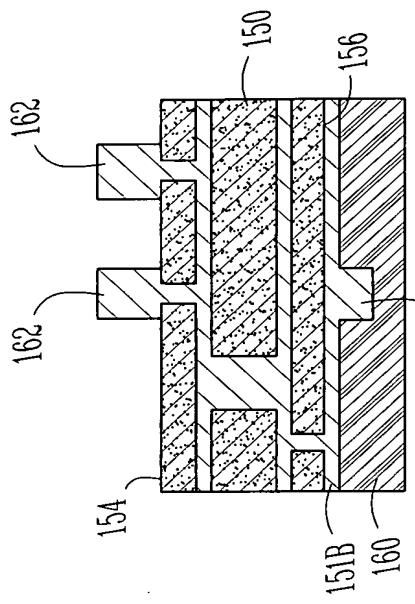


Fig. 21

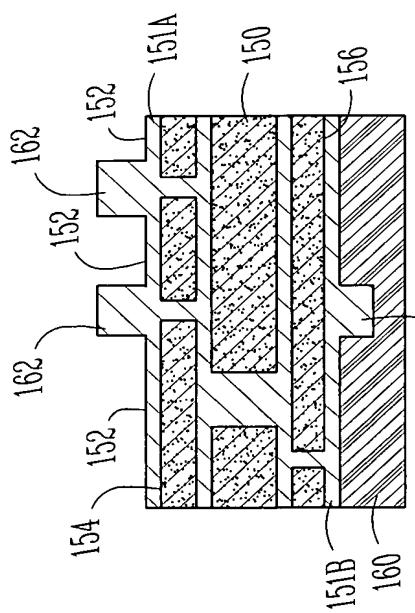


Fig. 20

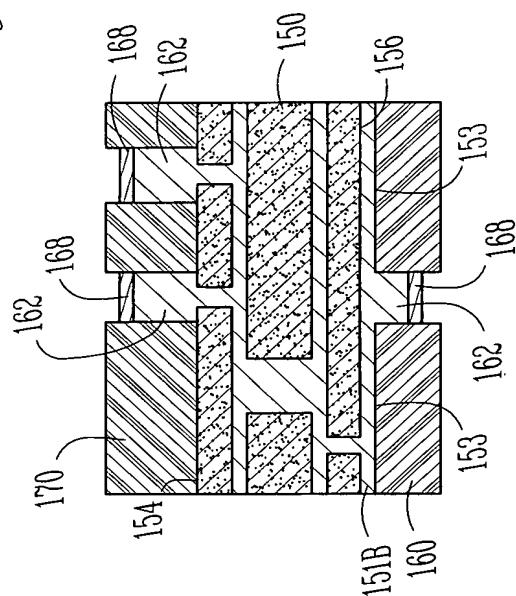


Fig. 22

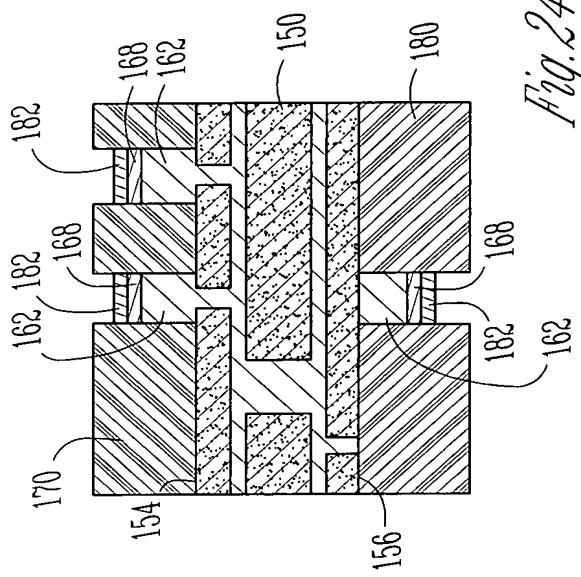


Fig. 24

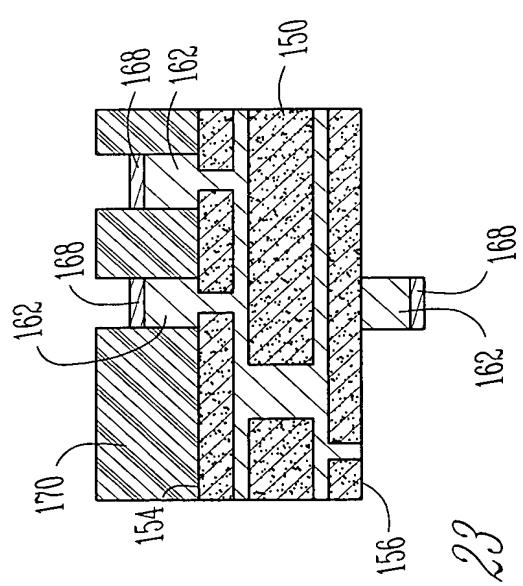


Fig. 23

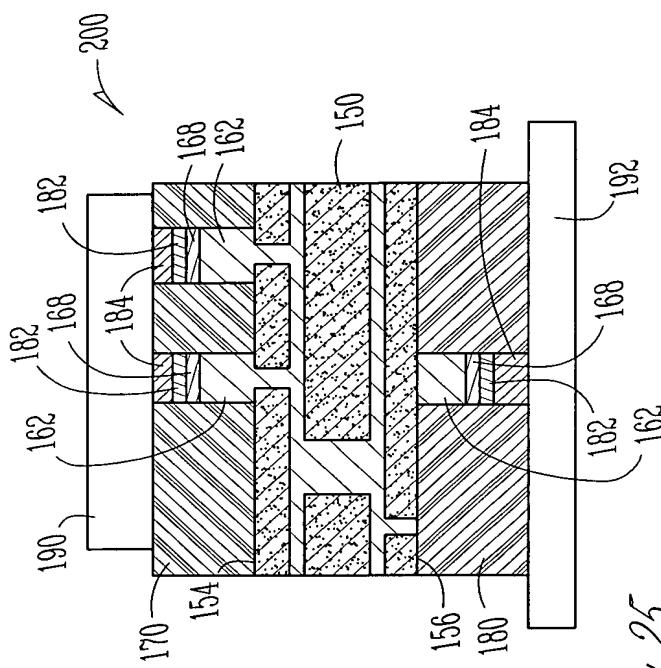


Fig. 25

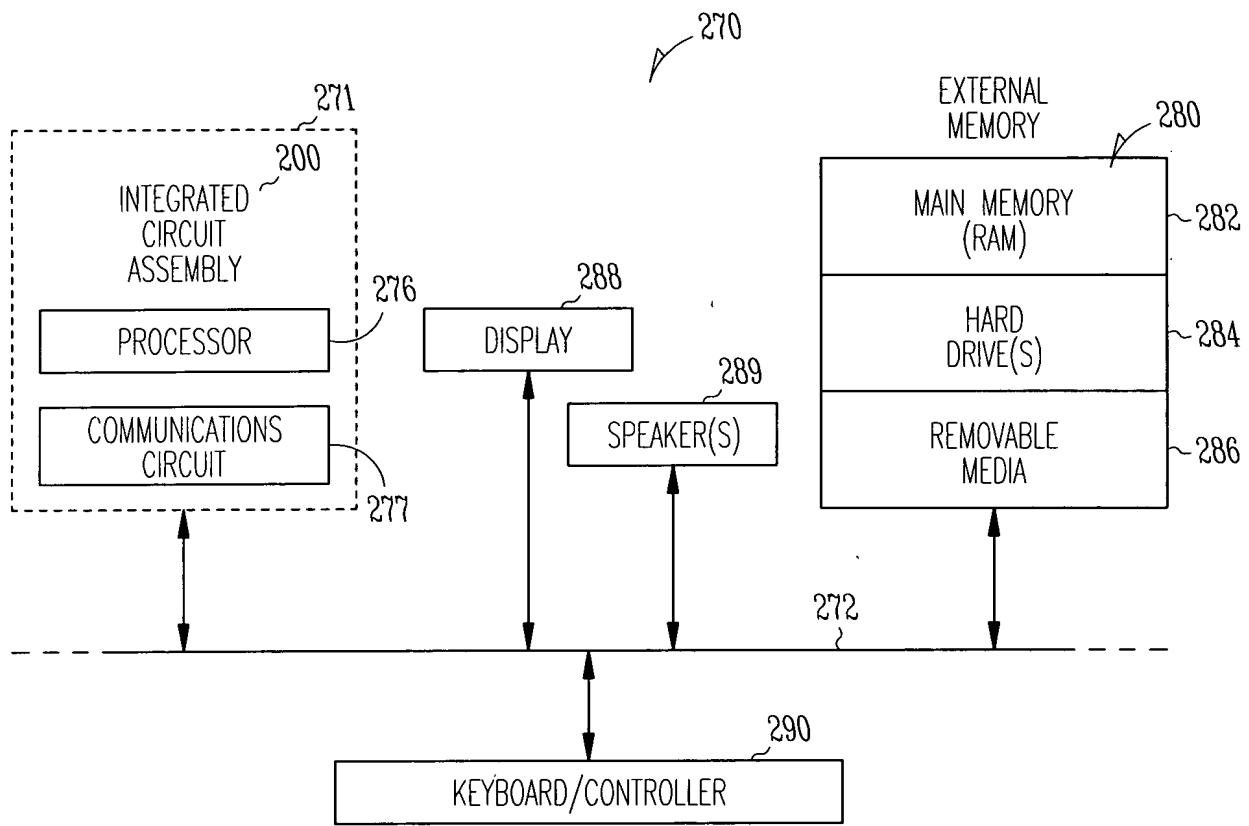


Fig. 26